

Time Table (July – Dec 2018)

V Semester - III Year B. Tech
ELECTRICAL ENGINEERING

DAY	1	2	3	4	Lunch recess 1.00 pm - 2.00 pm	5	6	7	8
	9.00 - 9.50am	10.00 - 10.50am	11.00 - 11.50am	12.00 - 12.50pm		2.00 - 2.50pm	3.00 - 3.50pm	3.55 - 4.45pm	4.50 - 5.40pm
Mon		B	C*	D*					
Tue	B	C*	D*						
Wed	C*	D*		-		B			
Thu	C*	-	A*	D*		A*	EE3701 DSP Laboratory		
Fri	-	A*	A*	B			EE3702 Analog Circuits Lab		

Slot	Course No. and Title	Faculty	Room No
A*	EE3001 Electromagnetic Fields (3-1-0-4)	Prof. Bijoy Krishna Das	6
B	EE3002 Analog Circuits (3-1-0-4)	Dr. K. P. Naveen Dr. N. N. Murthy	6
C*	EE3003 Power Systems (3-1-0-4)	Dr. Prashanth Vooka	6
D*	EE3004 Control Engineering (3-1-0-4)	Dr. P. S. Sai Krishna	6
	Free Elective 1		
	EE3701 Microcontroller & DSP Laboratory (0-0-3-2)	Dr. Ramakrishna Gorthi	Mac Lab
	EE3702 Analog Circuits Laboratory (0-0-3-2)	Dr. Prashanth Vooka	

Exam for EE3001 (A*-slot) will be conducted in the A-slot

Exam for EE3003 (C*-slot) will be conducted in the C-slot

Exam for EE3004 (D*-slot) will be conducted in the D-slot