

No.: Advt/ IITT/CSRC/2023-24/22

Date: 24-01-2024

Applications are invited from the eligible Indian national for JRF positions in a time bound MietY sponsored project under Chip to Startup program undertaken by IIT Tirupati.

Position	Junior Research Fellow
No. of position	02
Essential Qualification	MS/ME/MTech in Electrical/Electronics/Computer Science Engineering or equivalent with minimum of 65% mark. Preference will be given to candidates who have valid GATE score.
Project Title	Design of a Secure and Dependable RISC-V core for Cryptographic Application
Sponsoring Agency	Ministry of Electronics and Information Technology (MeitY). Govt of India
Monthly Salary	INR 37000/- + HRA as applicable
Tenure of Assignment	1 year, extendable up to completion of project subject to performance.
Desired Experience	- Knowledge of Processor Architecture - Knowledge of Digital VLSI design and test and EDA tools - Programming in Verilog/VHDL and C/C++
Nature of Work	- Research and development - Coding in verilog/vhdl and FPGA based implementation - Computer simulation using EDA tools - Documentation of the research work - Critical analysis of the experimental results - Offline on campus physical presence is needed
Age Limit	Age limit- Not more than 30 years as of 08-02-2024 (Relaxed for exceptional candidates)
Application Deadline	February 8 th , 2024 till 05.30 PM

Eligible candidates may send a detailed curriculum vitae (maximum 2 pages) specifying qualifications and experience, on or before the deadline to csrc_recruitment@iittp.ac.in. and vikram@iittp.ac.in (Dr. Vikramkumar Pudi, Principal Investigator). Importantly, mention your contact detail (email as well as cell number) in CV.

The shortlisted candidates will be informed via E-mail only. The selection will be based on the qualification, experience, and interview performance. The interview will be conducted offline at IIT Tirupati campus. IIT Tirupati reserves the right to reject any or all the applications without assigning any reason thereof.

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