

Time Table (July – Dec 2018)

**V Semester - III Year B. Tech
COMPUTER SCIENCE & ENGINEERING**

DAY	1	2	3	4	Lunch recess 1.00 pm - 2.00 pm	5	6	7	8
	9.00 - 9.50am	10.00 - 10.50am	11.00 - 11.50am	12.00 - 12.50pm		2.00 - 2.50pm	3.00 - 3.50pm	3.55 - 4.45pm	4.50 - 5.40pm
Mon	A	B		D		G			
Tue	B		D	E			CS3310 Compiler Design Lab		
Wed		D	E				Special Lecture		
Thu	E		G	A		D			
Fri		G	A	B			CS3510 Operating Systems Lab		

Slot	Course No. and Title	Faculty	Room No
A	CS3500 Operating Systems (3-0-0-3)	<i>Dr. V. Mahendran</i>	4
B	CS3300 Compiler Design (3-0-0-3)	<i>Dr. B. Venkataramana</i>	4
D	CS3700 Database Systems (3-1-0-4)	<i>Dr. Y. Kalidas</i>	4
E	CS3400 Software Engineering (3-0-0-3)	<i>Dr. Ch. Sridhar</i>	4
G	CS3101 Design and Analysis of Algorithms (3-0-0-3)	<i>Dr. G. Ramakrishna</i>	4
	Free Elective 1		
P	CS3510 Operating Systems Lab (0-0-3-2)	<i>Dr. V. Mahendran</i>	Mac Lab
Q	CS3310 Compiler Design Lab (0-0-3-2)	<i>Dr. B. Venkataramana</i>	Mac Lab