

Time Table (July – Dec 2018)
III Semester - II Year B.Tech
ELECTRICAL ENGINEERING

DAY	1	2	3	4	Lunch recess 1.00 -2.00 pm	5	6	7	8
	9.00 - 9.50am	10.00 - 10.50am	11.00 - 11.50am	12.00 - 12.50pm		2.00 - 2.50pm	3.00 - 3.50pm	3.55 - 4.45pm	4.50 - 5.40pm
Mon	A		C	D			MA-90m		
Tue		C	D			A			
Wed	C	D		F			MA-90m		
Thu		F	G	A		D	EE2702 Digital Circuits Lab		
Fri	F	G	A			C			

Slot	Course No. and Title	Faculty	Room No
D	EE1001 Electrical Circuits & Networks (3-1-0-4)	<i>Dr. Subrahmanyam Gorthi</i>	1
C	EE2001 Digital Systems (3-1-0-4)	<i>Dr. Jaynarayan T</i>	3
A	EE2002 Signals & Systems (3-1-0-4)	<i>Dr. Parthajit Mohapatra</i>	2
MA	MA2010 Complex Variables (3-0-0-3)	<i>Dr. Srijanani Anurag Prasad Dr. Durga Prasad Challa & Dr. S. Rajesh Dr. Ishapathik Das</i>	1
	MA2030 Linear Algebra (3-0-0-3)		2
	MA2040 Probability, Stochastic Process & Statistics (3-0-0-3)		4
F	HS2010 English for Communication (3-0-0-3)	<i>Dr. V. V. Krishna Reddy</i>	4
	HS3002 Principles of Economics (3-0-0-3)	<i>Dr. Chandra Shekar. B</i>	2
	HS2030 International Trade and Finance (3-0-0-3)	<i>Dr. Rahul Sirohi</i>	1
G	BT1010 Life Sciences (2-0-0-2)	<i>Dr. Mousumi Banerjee</i>	3
	EE2702 Digital Circuits Lab (0-0-3-2)	<i>Dr. Jaynarayan T</i>	Temp Campus